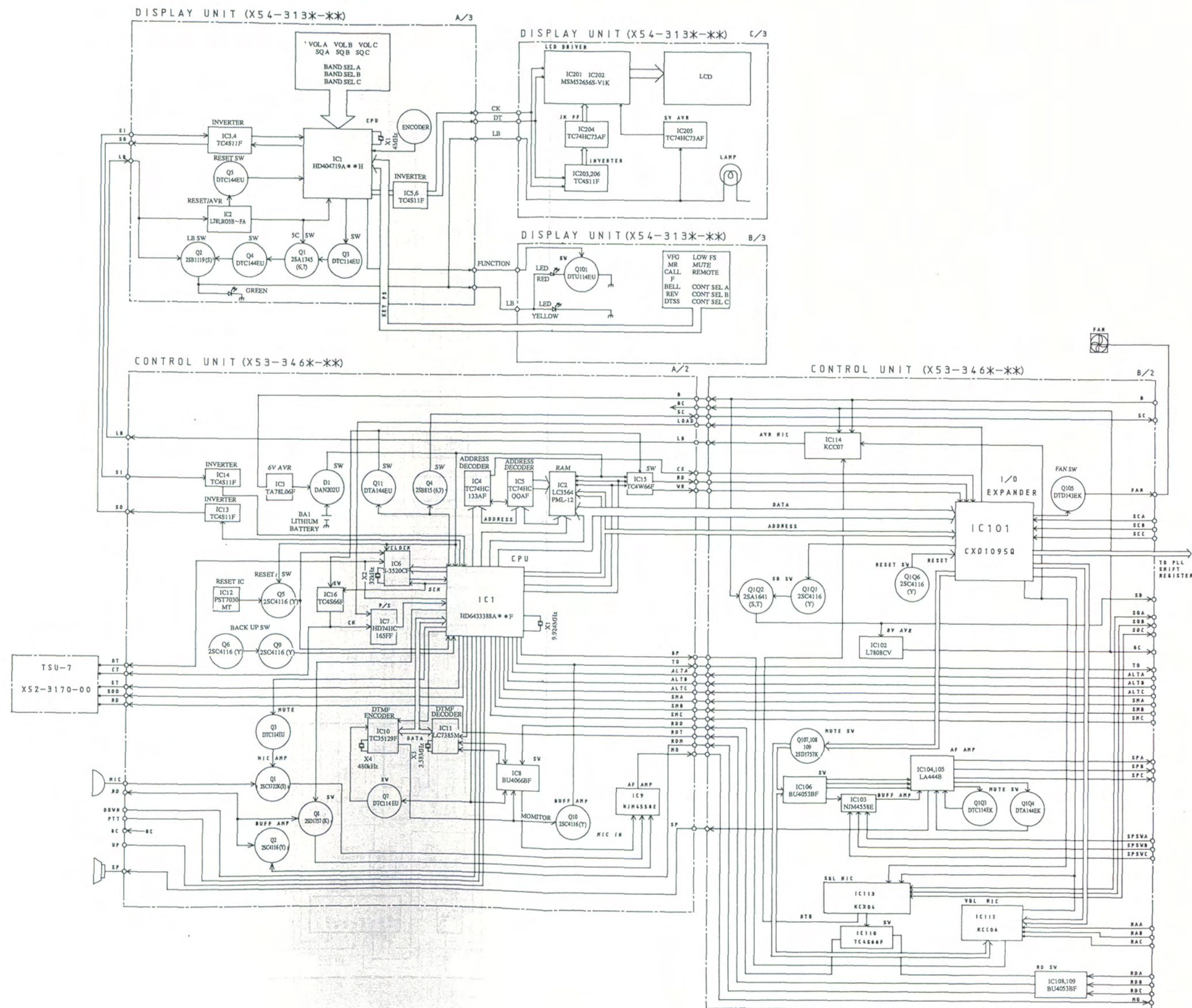
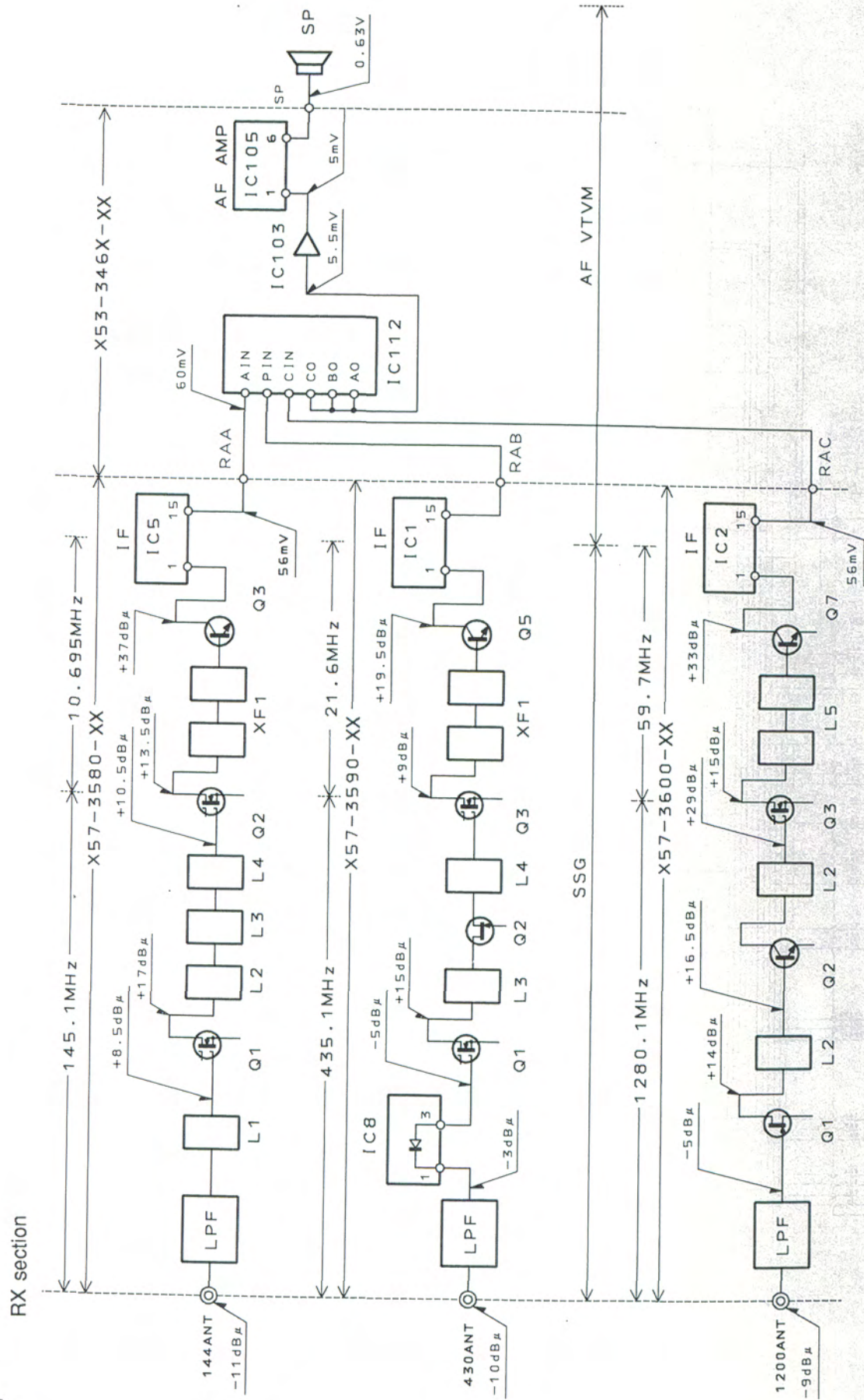


BLOCK DIAGRAM

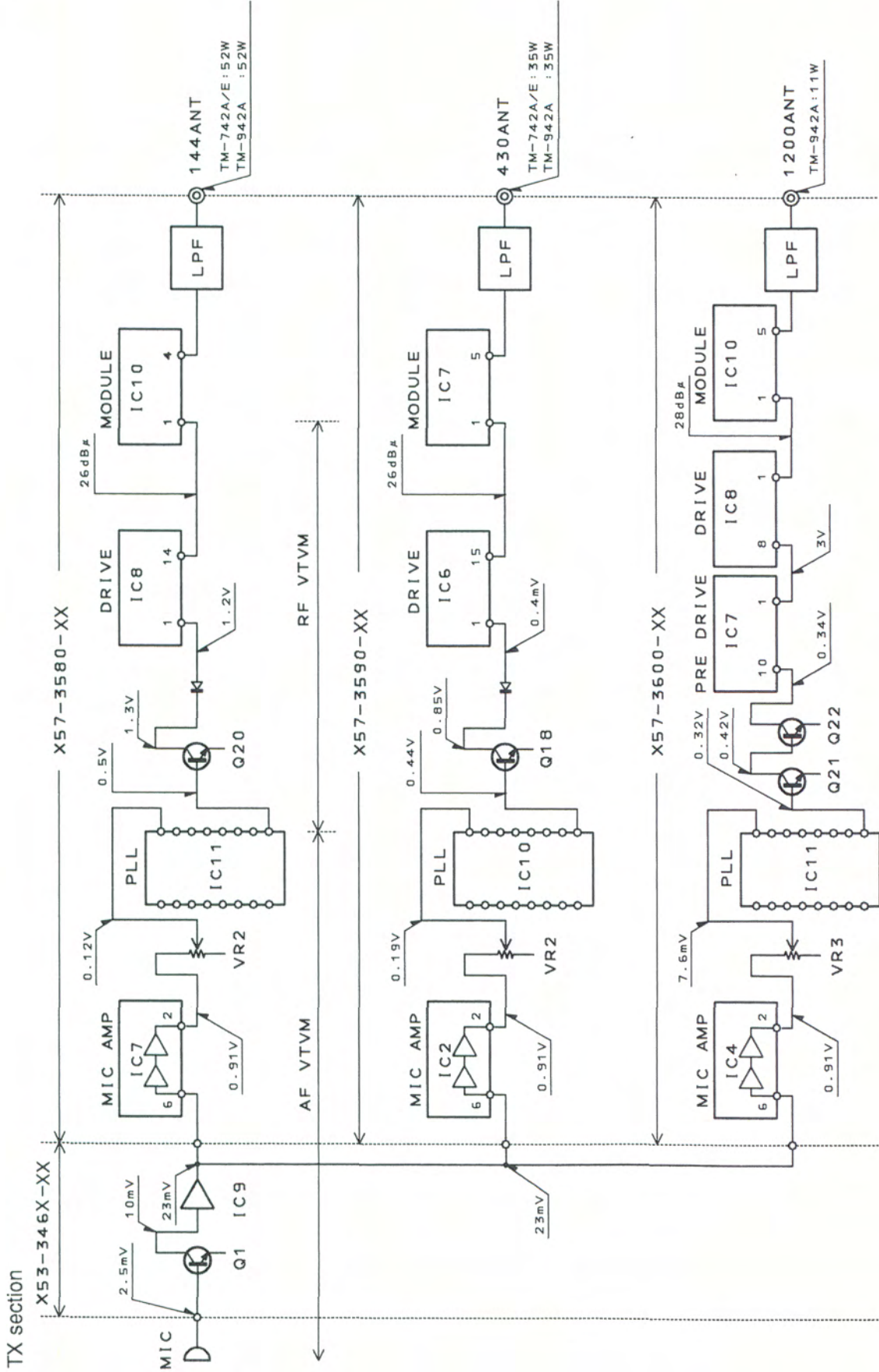




LEVEL DIAGRAM



- 1. SG input level for which a 12dB SINAD are obtained. Measured by connecting the SG to each point via a 0.01μF capacitor.
- 2. AF level obtained when the AF output level is adjusted for 0.63V/8Ω with the front panel AF VOL control. Measure with AF voltmeter connected to the speaker jack, receiving a 40dB EMF SSG signal modulated at 1KHz, DEV 3KHz.



- 1. AG is set so that MIC input becomes 3kHz DEV at 1kHz MOD.
- 2. Transmitting frequency: 145.0MHz, 435.0MHz, 1280MHz, 28.0MHz, 50.0MHz, 220.0MHz.
- 3. HI/MID/LOW SW: HI
- 4. APC SW: OFF